

ESP32 CPU and memory

- CPU
 - Dual-core SMP system with 2 Xtensa LX6 CPUs (Harvard Architecture)
 - 2 CPUs(“PRO_CPU” and “APP_CPU”) are interchangeable for most purpose.
 - name “PRO” and “APP” are for “protocol” and “application”
- MEMORY/Peripherals
 - All memories and peripherals are located on the data bus and/or the instruction bus of these CPUs.
 - The address mapping of two CPUs is symmetric.
 - = 2 CPUs use the same addresses to access the same memory (some minor exceptions exist)

